



Universidad
Carlos III de Madrid
www.uc3m.es

Session 25

Analysis of practical circuits with OA – Exercises

Electronic Components and Circuits

José A. Garcia Souto

www.uc3m.es/portal/page/portal/dpto_tecnologia_electronica/Personal/JoseAntonioGarcia

Analysis of practical circuits with Operational Amplifiers

OBJECTIVES

- To analyze linear circuits with operational amplifiers and several input signals
- To analyze the frequency response of linear circuits with operational amplifiers
- To complete a survey of applications of OA
- To analyze comparator circuits with operational amplifiers and obtain the transfer function

Analysis of circuits with Operational Amplifiers

METHODOLOGY

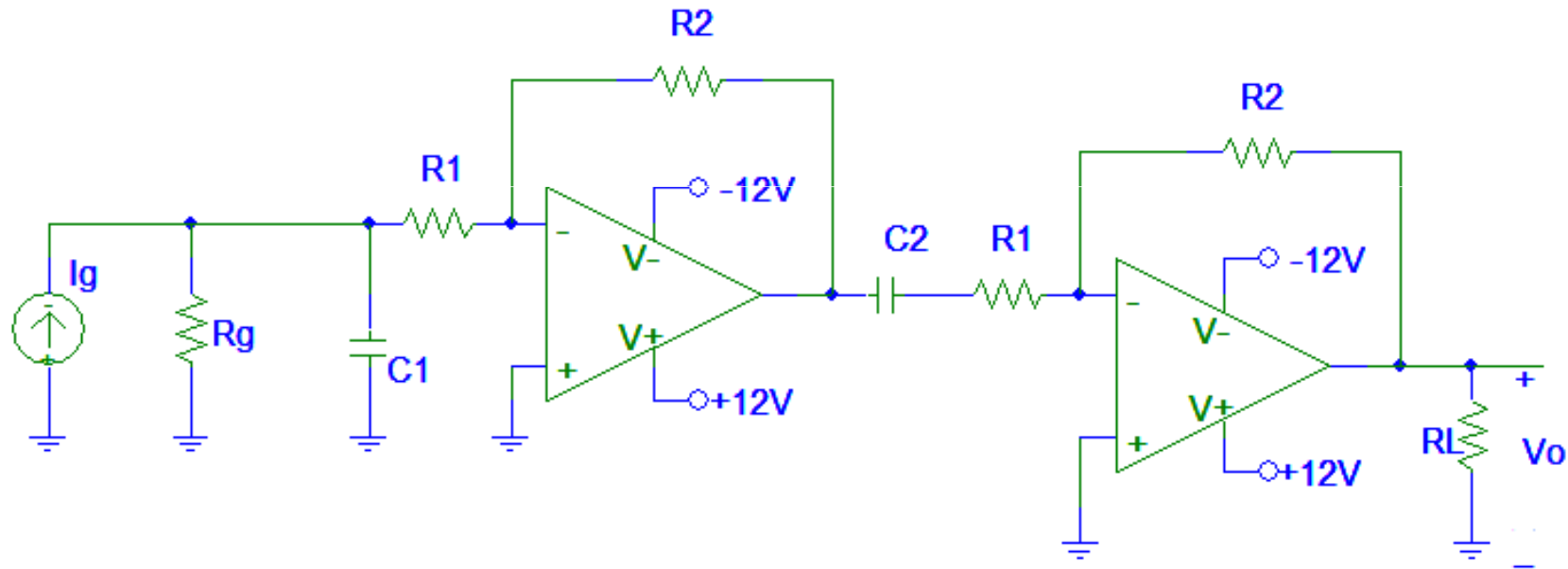
1. ¿Negative feed-back?.

- a) First approach with ideal operational amplifier
- b) Obtain the characteristics of gain, impedances and dynamic range
- c) Consider the frequency response

2. ¿Nonlinear application: comparator, rectifier, etc.?

- a) First approach working as comparator (saturated output) and two cases / ideal diode and two cases (on, off)
- b) Obtain the transfer function

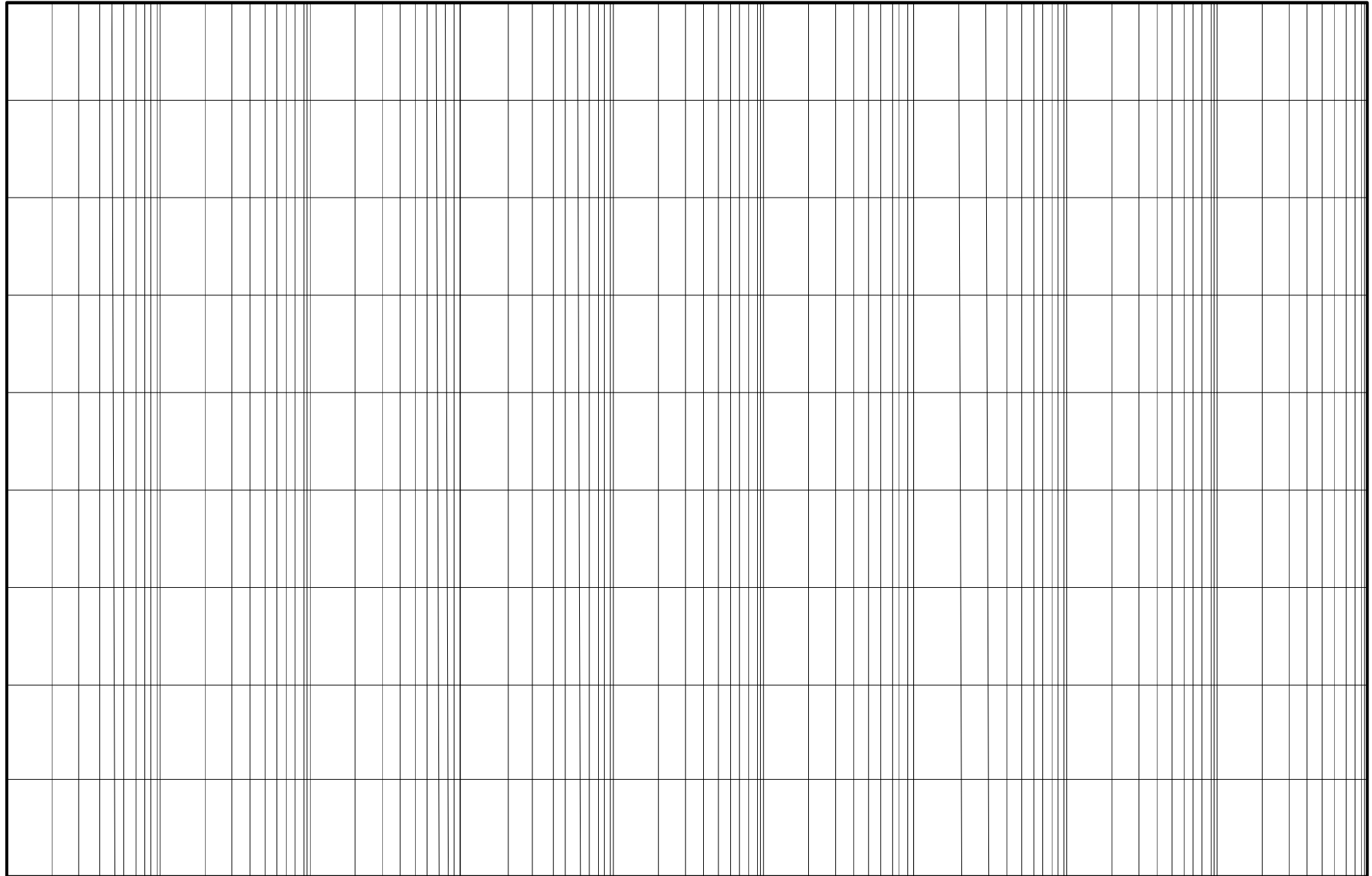
Example: AC coupled multi-stage trasimpedancia amplifier



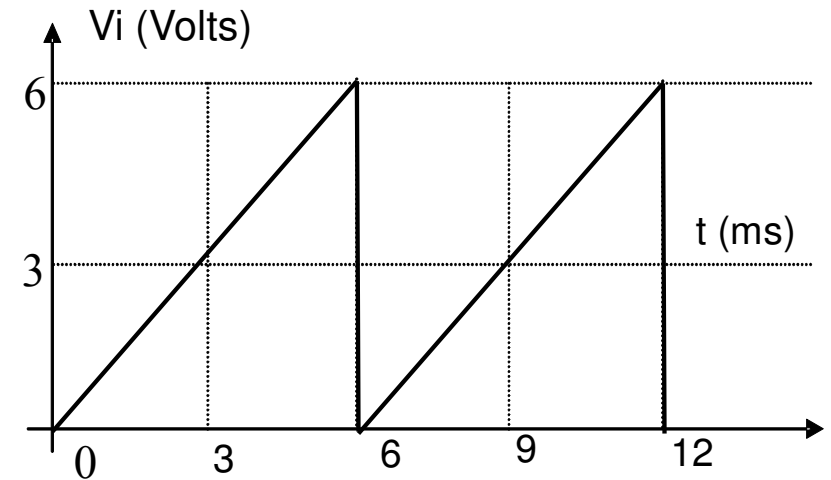
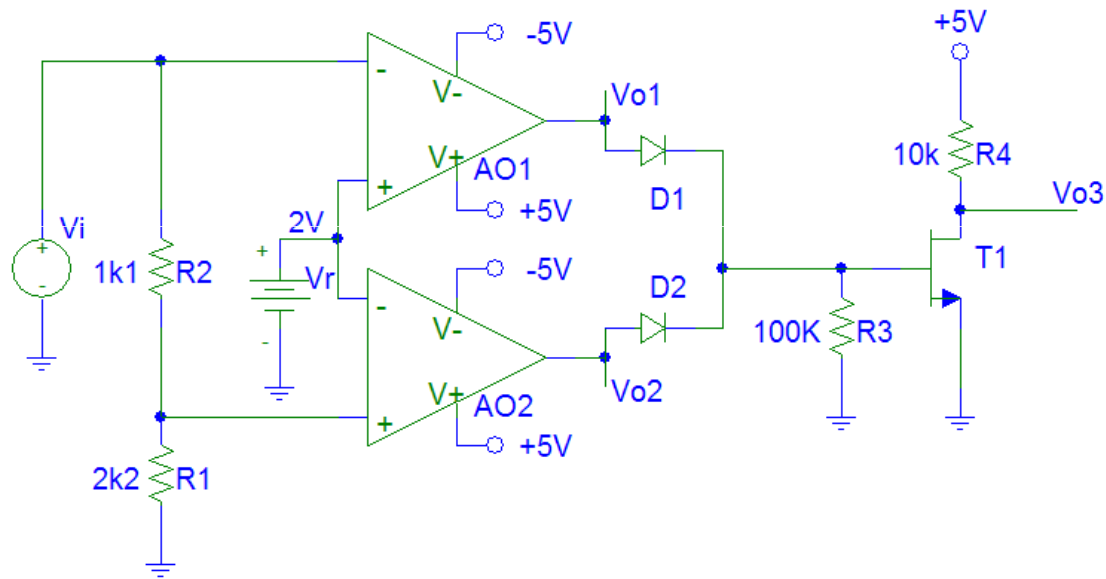
DATA: $R_g = 99\text{K}\Omega$, $R_1 = 1\text{K}\Omega$, $R_2 = 100\text{K}\Omega$, $R_L = 100\Omega$, $C_1 = 100\text{pF}$

- Obtain the upper cut-off frequency. Recalculate it if $G \times BW = 1.5$ in each amplifier.
- Obtain the value of C_2 that fixes 1 kHz lower cut-off frequency.
- Obtain the gain of the multi-stage amplifier V_o/I_g for 50 kHz signal.
- Plot the voltage V_o if the input current I_g is composed of $100\mu\text{A}$ DC and 10nA rms AC signal (sine, 1kHz).
- Plot the asymptotic Bode diagram.

Bode diagram



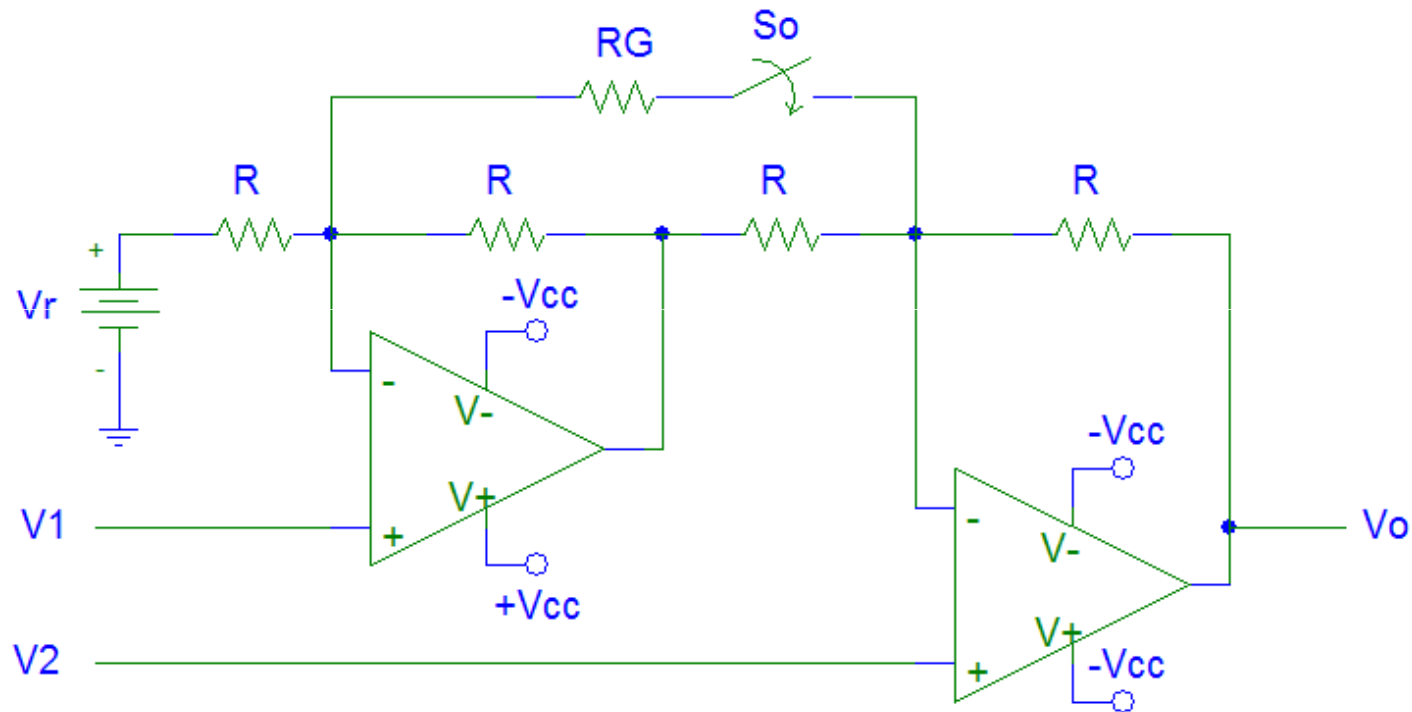
Exercise: window comparator



DATA: $\pm V_{cc} = \pm 5V$ $V_r = 2V$ $R_1 = 1,1K\Omega$ $R_2 = 2,2K\Omega$
 $R_3 = 100K\Omega$ $R_4 = 10K\Omega$ $V_{D-ON} = 0,7 V$ $V_{GS-ON} = 1 V$

- Plot the output voltages V_{o1} and V_{o2} .
- Obtain the value of the voltage V_{o3} from each and all combinations of V_{o1} and V_{o2} values. Indicate which operating region is the MOSFET T1.
- Plot the voltage V_{o3} , specifying precise time and amplitude values.

Proposed exercise



DATA:

$$\pm V_{cc} = \pm 12 \text{ V}$$

$$R = 51 \text{ K}\Omega$$

Assume that the switch S_o is open (R_G disconnected):

- In the case $V_r = 0\text{V}$, obtain the expression of the output V_o as a function of V_1 - V_2 .
- In the case $V_1 = V_2$, calculate the value of V_r to make $V_o = 6\text{V}$.
- Plot the signal V_o if the following voltages are applied simultaneously:
 - V_r previously calculated (if not obtained, use $V_r = 0\text{V}$)
 - V_1 20V DC and 1V peak (sine, 1kHz) and
 - V_2 20V DC

Assume that the switch S_o is closed (R_G connected):

In the case $V_r = 0\text{V}$, $V_1 = 0\text{V}$, obtain the value of R_G to obtain a gain $V_o/V_2 = 100$.