

# **OPERATING SYSTEMS:**

# Lesson 3: Introduction to Process Management

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### Contents

### **Process concept.**

- $\Box$ Basic lifecycle of a process.
- □ Process information
- $\Box$ Multitasking.
- □Context switch.
- □Generating an executable.





### Process

- Process: Program in execution.
  - Each execution of a program leads to a process.
  - Process is the unit of management for operating system
- A process consists of:
  - Program text: Instructions.
  - Set of data associated to program execution.





### **Program execution**







Memory representation



- A process needs memory for instructions and data.
- Different instances of a program need independent areas for data.





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## Basic lifecycle of process



## Simplified queuing model: Single processor





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## Simplified queuing model: Multiple processors

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- All the information allowing the process correct execution.
- Three categories:
  - Information stored in the processor.
  - Information stored in memory.
  - Additional information managed by operating system.





### **Processor state**

### □ Processor state includes values of processor registers.

#### Registers accessible in user mode.

- General registers: Register file.
- Program counter.
- □ Stack pointer.
- User part in status register.
- Registers accessible in privileged mode:
  - Privileged part from status register.
  - Memory management registers (e.g. PTBR).

### □Context switch:

- □ Save processor state for outgoing process.
- □ Restore processor state for incoming process.





□Memory image consists of the **memory spaces** that a process is authorized to use.

□If a process generates an address out of the address space, hardware generates a **trap**.

Depending on specific computer, memory image may be referred to *virtual memory* or *physical memory*.





- Process with a single fixed size region.
  - Used in systems without virtual memory.
- Process with a single variable sized region.
  - Systems without virtual memory:
    - Need reserve space → Memory waste.
  - Systems with virtual memory:
    - Virtual reserve space → Feasible but less flexible than multiple region.
    - Not used.





Memory image models: Multiple regions

- Process with fixed number of regions of variable size.
  - Prefixed regions (text, data, stack).
  - Each region may grow.
  - With virtual memory, the *hole* between stack and heap does not consume resources.







Memory image models: Multiple regions

- Process with variable number of regions of variable size.
  - More advanced option (used in current versions in Windows and UNIX).
  - Process structured as a number of regions.
  - Very flexible:
    - Shared regions.
    - Regions may differ in permissions.







- Operating system keeps additional information on processes.
  - Operating system keeps information in a table: Process Table.
  - **Process Control Block** (PCB): Each entry in table keeps information about one process.
  - Almost all information about process stored in PCB.
     Some information elements kept outside due to implementation reasons.





# Contenidos del BCP

- Identification information.
- Processor state.
- Process control information.

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Scheduling and state information: •Process priority. Allocated regions description. •Per region information. Allocated resources: **Pointers for structuring process** queues (or rings). Information for inter process communication.



- Not all the information referred to a process is stored in its PCB.
- Decision taken in function of:
  - Efficiency.
    - Tables should have a prefixed size and always be in memory.
    - Size needs to be optimized.
  - Information Sharing.
    - If data needs to be shared it cannot be in the PCB.
    - Pointers are used to point to other structures (tables) allowing for information sharing.
      - Open files.
      - Memory pages.





- Placed outside PCB.
- Describes process memory image.
- PCB contains pointer to page table.
- Reasons:
  - Variable size.
  - Memory sharing among processes requires it to be external to PCB.





- Placed outside PCB.
- If added to open files table (in PCB) cannot be shared.
- If associated to i-node is always shared.
- Stored in a common structure for multiple processes and a new one allocated with OPEN service.





# Example: Running a command

```
#include <sys/types.h>
#include <stdio.h>
int main(int argc, char** argv) {
 pid t pid;
 pid = fork();
 switch (pid) {
                                          prog cat f1
    case -1: /* error */
     exit(-1);
    case 0: /* proceso hijo */
      if (execvp(argv[1], &argv[1])<0) { perror("error"); }</pre>
       break;
   default:
     printf("Proceso padre");
  }
 return 0;
}
```





### Fork service

- pid\_t fork(void);
- Duplicates process invoking the call.
- □ Parent process and child process go on running the same program.
- □ Child process inherits open files from parent process.
  - □ Open file descriptors are copied.
- □ Pending alarms are deactivated.
- Returns:
  - -1 on error.
  - In parent process: child process descriptor.
  - In child process: 0.





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### Fork service







### **Exec service**

• Single service with multiple library functions.

```
int execl(const char *path, const char *arg, ...);
int execv(const char* path, char* const argv[]);
int execve(const char* path, char* const argv[], char* const envp[]);
int execvp(const char *file, char *const argv[])
```

- Changes current process image.
  - **path**: path to executable file.
  - file: Looks for the executable file in all directories specified by PATH.
- Description:
  - Returns -1 on error, otherwise it does not return.
  - The same process runs another program.
  - Open files remain open.
  - Signals with default action remain defaulted, signals with handler take default action.





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### Exec service







### Exit service

• Finalizes process execution.

#### void exit(status);

- All open files descriptors are closed.
- All process resources are released.
- **PCB** (Process Control Block) is released.





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**Operating system types** 

# **Operating Systems**







# Principles of multitasking

- Real parallelism between I/O and CPU (DMA).
- Process alternate between I/O and processing phases.
- Several processes stored in memory.





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#### Carlos III de Madrid Advantages of multitasking

- Eases programming, dividing a program in multiple processes (modularity).
- Allows simultaneous interactive service of multiple users in an efficient way.
- Takes advantage of times a process spends waiting for an I/O operation to be completed.
- Increases utilization of CPU.





# Multiprogramming degree

- Multiprogramming degree: Number of active processes.
- Main memory needs: System without virtual memory.











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- Systems with virtual memory:
  - Divide addressing space of processes in pages.
  - Divide physical memory addressing space in main memory in page frames.
- At a given time, each process has a certain number of its pages in main memory (resident set).





# Memory needs: Virtual memory system







# Performance: Small physical memory

### □When

multiprogramming degree increases:

- Resident set size decreases for each process.
- Trashing happens before achieving a high CPU utilization percentage.
- **Solution**: Add more main memory.









# Performance: Large physical memory

- When multiprogramming degree increases:
  - Resident set size decreases for each process.
- High CPU utilization percentage is achieved with less processes that fit in memory.
- **Solution**: Improve processor or add more processors.



Multiprogramming degree





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# Context switching

- When operating system assigns processor to a new process.
- Actions:
  - Save process state in PCB for process in execution.
  - Restore state of new process in processor.





### Context switch







# Context switching types

### □ Voluntary context switch:

- Process performs call to operating system (or generates exception like page fault) implying waiting for an event.
- $\square Running \rightarrow Blocked.$
- Examples: reading from terminal, page fault.
- $\square \text{ Reason} \Rightarrow Processor use efficiency.$

### □ Involuntary context switch:

- □ OS appropriates CPU.
- $\square Running \rightarrow Ready.$
- Examples: time slice ellapsed or process moves from blocked to ready and has higher priority.
- $\square Reason \Rightarrow Processor use sharing$





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## Executable generation







# Link Editor (linker)

- Combines object files:
  - Merges several relocatable object files (.o) in a single executable object file: input for loader.
- Resolves external references:
  - □ References to symbols defined in other object file.
- Relocates symbols:
  - From relative positions in .o to absolute positions in executable: adjust refs to these new positions.
  - Symbols: refs to functions and data.





- ELF: Executable and Linkable Format
  - formato binario estándar para ficheros objeto
  - $\Box$  original de System V  $\rightarrow$  BSD, Linux, Solaris
  - formato unificado para:
    - ficheros objeto reubicables
    - ficheros objeto ejecutables
    - ficheros objeto compartidos













### **Executable load**















# Static and dynamic libraries

#### Drawbacks of static libraries:

- □ Code potentially duplicated in executables:
  - $\Box$  Disk (file system).
  - □ Virtual memory space in processes.
- □ Bugs in libraries → new version → need to relink
- Solution: dynamic libraries (\*.so) (dynamic link libraries, DLLs):
  - Components loaded in memory and executed at runtime.
  - Functions from libraries may be shared among multiple processes.





## **Dynamic libraries**





# Reminder

- □ Difference between program and process.
  - $\hfill\square$  A process is a porgram in execution.
- □ Operating system manages running processes (process lifecycle).
- □ Process information consisting of: process state, memory image and PCB.
- □ Multitasking allows a better use of computer resources.
- □ Context switching introduces a small overhead.
- □ Static libraries are linked at compile time while dynamic libraries are linked at process creation time.
- □ Process creation implies creation of its memory image and the allocation of a PCB.





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