



Exercises on Parallelism and Concurrency

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1. Exam exercises related to parallelism

Exercise 1 (June 2015)

Given the following program parallelized with OpenMP:

```
double calculate_pi(double step) {
    int i;
    double x, sum = 0.0;
    #pragma omp parallel for reduction(+: sum) private(x)
    for (i=0;i<1000000;++i) {
        x = (i-0.5) * step;
        sum += 2.0 / (1.0 + x*x);
    }
    return step * sum;
}</pre>
```

Write an equivalent version of the program without the reduction annotation.

Exercise 2 (January 2015)

Given the following code parallelized with OpenMP and assuming that we count with 4 threads (export OMP NUM THREADS=4) and iter = 16:

```
#pragma omp parallel for private(j)
for (i = 0; i < iter; ++i) {
    for (j = iter - (i+1); j < iter; ++j) {
        //This function has a computing time of 2s
        compute_iteration(i, j, ...);
    }
}</pre>
```

State:

1. Fill out the following table with a possible allocation of iterations of the loop with (index i) with static scheduling, schedule (static). Indicate in the table which thread performs each iteration of the loop (each value other than i) and how long that iteration takes. Also calculate the approximate execution time per thread and the total execution time.

# iter	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Thread (ID)															
Time (s)															

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Fill in the following table with a possible allocation of iterations by running the loop (with index
i) with dynamic scheduling and *chunk* 2, schedule (dynamic, 2). Indicate in the table which
thread performs each iteration of the loop (each value other than i) and how long that iteration
takes. Also indicate the approximate execution time per thread and the total execution time.

# iter	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Thread (ID)															
Time (s)															

3. Justify which of the previous schedules would be best for a generic case (variable number of iterations and threads).

2. Exam exercises related to concurrency

```
Exercise 3 (June 2015)
```

Given the following definition of a lock-free stack:

```
template<typename T>
class stack {
private:
    struct node {
        std::shared_ptr<T> data;
        node* next;
        node(T const& data_):data(new T(data_)){}
    };
    std::atomic<node*> head;
public:
    void push(T const& data);
    std::shared_ptr<T> pop()
};
```

- 1. Provide a lock-free implementation for both **push** and **pop** functions.
 - **NOTE**: Ignore the problem related to memory leaks.
- 2. Briefly explain how you could avoid the memory leak problem associated with your solution.

```
Exercise 4 (January 2015)
```

Let the following code be programmed with atomics. At point \mathbf{A} , head contains the value $\mathbf{8}$ and an attempt is made to insert a value $\mathbf{9}$. If another thread tries to insert a value $\mathbf{10}$ concurrently, indicate which data will be printed on the screen if part \mathbf{B} is executed (line $\mathbf{16}$). Which data if you get to execute the part \mathbf{C} (line $\mathbf{25}$)?

```
struct node {
   std :: shared _ptr<T> data;
   node* next;
   node(T const& data_):data(new T(data_)), next(nullptr) {}
};
std :: atomic<node*> head;
void push(T const& data) {
   node* const new_node=new node(data);
   new_node=>next=head.load();
   //A
   std :: cout << *(head.load()->data) << " "; // 8</pre>
```





```
std::cout << *(new_node->next->data) << " "; // 8
std::cout << *(new_node->data) << std::endl; // 9

if (head.compare_exchange_strong(new_node->next,new_node)) {
    //B
    std::cout << *(head.load()->data) << " ";
    std::cout << *(new_node->next->data) << " ";
    std::cout << *(new_node->data) << std::endl;
    }
else {
    //C
    std::cout << *(head.load()->data) << " ";
    std::cout << *(new_node->next->data) << " ";
    std::cout << *(new_node->data) << std::endl;
    }
}</pre>
```

Exercise 5 (January 2014)

Given the following function:

```
std :: mutex m; // global mutex
int counter; // global counter
void f() {
    m.lock();
    ++counter;
    m.unlock();
}
```

We want to replace the global variable m and avoid possible system calls, and at the same time, it is desired to ensure mutual exclusion in the *counter* variable increment.

State:

- 1. Propose and implement a solution that offers sequential consistency and does not involve system calls.
- 2. Propose and implement a solution that offers release-acquire consistency.
- 3. Propose and implement a solution that offers release-acquire consistency and it is valid in case of the *counter* variable becomes a double-precision float-point number.