Unit 3. Analog CMOS fundamental circuits

The outline of this unit is as follows:

- Current sinks/sources: how to implement current sinks and sources which will be used later on for current mirrors. Definition of the output resistance.
- Current mirrors: fundamental structure used to bias transistors in different branches from a single current reference. Errors in the mirror. Output resistance. Improved structures for accurate current mirroring.
- Examples of current mirrors.
- Switches and active resistors: how to implement switches and resistors with transistors.
- Current and voltage references.