

Unit 7. Inverter-based circuits

The outline of this unit is as follows:

- The CMOS inverter cell: dynamic response, power features and frequency response.
- The ring-oscillator.
- Buffers: optimal solution for buffer chain.
- Distributed drivers: clock distribution inside SoCs.
- Applications: DPLLs, DLLs, VCO-ADCs, and TDCs.